	ISSUE CLASSIFICATION		
Subclass			PATENT NUMBER
Class			

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U.S. UTILITY Patent Application

		O.I.P.E.		PATENT DATE
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SCANNED HT	Q.A. CH			

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/517884		380 713	151	2766	N. ZIA
				2134	

APPLICANTS

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TITLE

IEEE 1394 link layer chip with "5C" authentication and key exchange accelerator

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION										
							☐ Continued on Issue Slip Inside File Jacket			

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)		(Date)		
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
	(Primary Examiner)		(Date)		
☐ The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)		(Date)		

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